





III Semester

Sr No	Course Code	Course Title	Hours per Week			Credits	Maximum Marks		
			L	T	P		Continual Assessment	End Sem Examination	Total
1	AS303T	Applied Mathematics – III	4	-	-	4	30	70	100
2	CE301T	Computer Architecture	3	-	-	3	30	70	100
3	CE302P	Computer Lab - I	-	-	4	2	25	25	50
4	CE303T	Data Structures	4	-	-	4	30	70	100
5	CE303P	Data Structures lab	-	-	4	2	25	25	50
6	CE304T	Digital Circuits and Fundamentals of micro-processor	4	-	-	4	30	70	100
7	CE304P	Digital Circuits and Fundamentals of micro-processor Lab	-	-	2	1	25	25	50
8	H 102	Universal Human Values - II	3	-	-	3	30	70	100
9	CE305T	Career Development-I	2	-	-	0	Audit		
<b>Total</b>			<b>20</b>	<b>-</b>	<b>10</b>	<b>23</b>	<b>225</b>	<b>425</b>	<b>650</b>

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**B.Tech. Scheme of Examination & Syllabus 2023-24**

**COMPUTER ENGINEERING**

**THIRD SEMESTER**

Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
AS303T	Applied Mathematics-III	4	-	-	4	30	70	100

Course Objectives	Course Outcomes
<p>The aim of this course is</p> <ul style="list-style-type: none"><li>● To introduce the essential concepts of Numerical Computational techniques &amp; Theory of Probability.</li><li>● To familiarize the students with concepts in linear algebra and statistics.</li></ul>	<p><b>Students will be able to</b></p> <ul style="list-style-type: none"><li>● Analyze and solve problems by numerical computation method for Transcendental equations and System of linear equations.</li><li>● Identify engineering problems related to Matrices: Eigen value &amp; Eigen vectors &amp; Functions of Matrices.</li><li>● Apply various concepts of vector spaces.</li><li>● Apply various concepts of joint distribution.</li><li>● Use statistical methods and tools in engineering problems.</li></ul>

**Unit I** [8Hrs]  
**Numerical Methods:** Error in numerical calculations, Solution of Algebraic and Transcendental Equations: Method of False position, Newton–Raphson method, Solution of system of simultaneous linear equations: Gauss elimination method and Crout's method Largest Eigen value and Eigen vector by Iteration method. Euler modified method, Runge Kutta method.

**Unit II** [7Hrs]  
**Matrices:** Linear dependence of vectors, Characteristics equation, Eigen values and Eigen vectors, Reduction to diagonal form, Reduction of quadratic form to canonical form by orthogonal transformation, Sylvester's theorem.

**Unit III** [6Hrs]  
**Vector Space:** Subspaces, Linear Dependence/Independence, Basis, Dimension, Linear transformation, Range Space and Rank, Null Space and Nullity, Rank nullity theorem, Matrix Representation of a linear transformation, Linear Operators on  $R^n$  and their representation as square matrices.

**Unit IV** [7Hrs]  
**Probability:** Baye's rule, Review of discrete and continuous random variables, Joint probability function of discrete random variable, Marginal probability function and Conditional distribution of discrete random variable, Mathematical expectation of discrete random variable, Variance and Standard deviation, and Covariance of joint distribution.

**Unit** [7Hrs]  
**Statistics:** Multiple regression analysis, Regression equation of three variables, Measures of central tendency, Mean, Median, Mode, Mean deviation, Standard deviation, Testing a hypothesis, Null hypothesis, Alternative hypothesis, t-test, F-test and Chi square test.

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Linear Algebra and Its Application (Paperback)	Gilbert Strang	2007	Nelson Engineering
2	Higher Engineering Mathematics	B.S. Grewal	40th Edition	Khanna Publication
3	Theory & problems of Probability and Statistics	Murray R. Spiegel		Schaum Series, Mc Graw Hills
4	Introductory methods of Numerical Analysis	S.S. Sastry		PHI

**Reference Books**

S.N	Title	Authors	Edition	Publisher
1	Advanced Engineering Mathematics	Erwin Kreyszig	8 <sup>th</sup> Edition	Wiley India
2	Linear Algebra	Seymour Lipschutz et al	3 <sup>rd</sup> Edition	Schaum series.
3	First course in Linear Algebra	Nagpaul,		Wiley Eastern Ltd, New Delhi
4	A Text Book of Engineering Mathematics	N. P. Bali & M. Goyal		Laxmi Publication.

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**THIRD SEMESTER**

Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE301T	Computer Architecture	3	-	-	3	30	70	100

Course Objectives	Course Outcomes
<p>This course is intended</p> <ul style="list-style-type: none"><li>● To provide understanding of design of Computer Architecture and organization.</li><li>● To provide understanding of issues involved in design of control unit.</li><li>● To understand the concepts of memory organization and its interfacing.</li></ul>	<p><b>Students will be able to</b></p> <ul style="list-style-type: none"><li>● analyze and describe different computer architectures.</li><li>● Solve various computer arithmetic problems</li><li>● analyze the complete execution of instruction using different control units.</li><li>● understand I/O device interfacing and computer memory hierarchy.</li><li>● understand various methods in parallel organizations of processor</li></ul>

**Unit I** [7Hrs]

**Basic structure of computers:** A Brief History of computers Designing for Performance Von Neumann Architecture , Computer Components - Interconnection Structures - Bus Interconnection - Addressing modes - Instruction Set Architecture (Instruction set based classification of processor i.e. RISC, CISC, RISC vs CISC Comparison).

**Unit II** [7Hrs]

**Arithmetic Unit :** Addition & subtraction of signed numbers - Binary Multiplication: Booths algorithm - Unsigned Integer multiplication and division algorithm - Floating point operations

**Unit III** [8Hrs]

**Processing unit :** Machine Instruction characteristics, types of operands, types of operations - Instruction formats- Instruction types - Processor organization - Register Organization - Instruction cycles -Instruction Pipelining, Hazards, Multiple bus organization, Control unit - Hardwired control unit, Microprogrammed control unit

**Unit IV** [7Hrs]

**I/O Organization and Memory Hierarchy:** Input/output Systems - Programmed I/O, Interrupt Driven I/O, Direct Memory Access (DMA) Memory Systems: locality of reference principle - Memory Hierarchy Cache memory - Main Memory - Virtual memory - Secondary storage

**Unit V** [7Hrs]

**Parallel Organizations:** Superscalar Processors - Multiple Processor Organizations - Flynn's Classification, Symmetric Multiprocessors - Non uniform Memory Access - Vector Processor

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Computer Organization & Architecture	William Stallings	8 <sup>th</sup> Edition	Prentice Hall
2	Computer Organization	Carl Hamacher Tata	5 <sup>th</sup> Edition	McGraw Hill

**Reference Books**

S.N	Title	Authors	Edition	Publisher
1	Computer Architecture: A Quantitative Approach	John L. Hennessy, David A. Patterson,	6 <sup>th</sup> Edition	Elsevier Science
2	Computer Organization	J. P. Hayes	5 <sup>th</sup> Edition	Tata McGraw

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**THIRD SEMESTER**

Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE302P	Computer Lab- I	-	-	4	2	25	25	50

Course Objectives	Course Outcomes
This course is intended <ul style="list-style-type: none"><li>To provide understanding of basic problem solving using competitive programming</li><li>To enhance the ability for complex problem solving using competitive programming</li></ul>	<b>Students will be able to</b> <ul style="list-style-type: none"><li>Explore and implement the competitive programming concepts of Basic programming</li><li>Explore and implement the advanced concepts of competitive programming</li></ul>

Expt. No.	Title of the experiment
1	To explore the competitive programming examples based on Basic Programming
2	To explore the competitive programming examples based on Array
3	To explore the competitive programming examples based on Data Structure
4	To explore the competitive programming examples based on Strings
5	To explore the competitive programming examples based on Sorting
6	To explore the competitive programming examples based on Binary Search
7	To explore the competitive programming examples based on Maths
8	To explore the competitive programming examples based on Dynamic Programming

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Let us C	Yashwant Kanetkar		BPB Publication
2	Python Programming: A Practical Approach	Vijay Kumar Sharma, VimalKumar, SwatiShar ma, ShashwatPathak		CRC Press

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Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE 303T	Data Structures	4	-	-	4	30	70	100

Course Objectives	Course Outcomes
<p>This course is intended</p> <ul style="list-style-type: none"><li>To provide knowledge of basic concepts in data structures and algorithms.</li><li>To choose the appropriate data structure and algorithm design method for a specified application.</li><li>To efficiently implement the different data structures and solutions for specific problems</li></ul>	<p><b>Students will be able to</b></p> <ul style="list-style-type: none"><li>Understand the basic concept of data structures, time complexity and analyse the various sorting and searching algorithms.</li><li>To implement dynamic data structures like singly, doubly and circular linked list.</li><li>Apply the different linear data structures like stack and queue to various computing problems.</li><li>Implement different types of trees and apply them to problem solution.</li><li>Demonstrate the representation of graphs and their applications in real life problem and infer the use of symbol tables for hashing and collision resolution.</li></ul>

**Unit I** **[8Hrs]**

**Introduction:** Concept of Data structures, Time and space analysis of algorithms, Big oh and theta notations and omega notations, Average, best and worst case analysis,

**Searching and sorting techniques-** Linear search, Binary search, Indexed search, Insertion sort, selection sort, Bubble Sort, radix Sort, Merge Sort, Quick Sort.

**Unit II** **[8Hrs]**

**Linked Lists :** Singly linked list, Implementation of linked list using static and dynamic memory allocation, operations on linked list, polynomial representations and manipulations are using linked list, circular linked list, doubly linked list, Generalized list, sparse matrix, polynomial

**Unit III** **[8Hrs]**

**Stack and Queue:** Array representation of stacks, Implementation of stack using linked lists, Queues ,Dequeue, Circular queue, Polish notation, Application of stack & queue: Conversion from Infix to Postfix , Evaluation of postfix expressions, Priority Queues

**Unit IV** **[7Hrs]**

**Trees:** Basic Terminology, Basic trees, Binary tree representations, threaded storage representation, binary tree traversals, binary search trees, Application of trees. Preliminary treatment of AVL Trees, B- Trees.

**Unit V** **[9Hrs]**

**Graphs:** Definition & terminology, Graph representation: matrix representation of Graph, List of structure, other representation of graphs, Breadth First Search, Depth First Search, Spanning trees, Shortest path algorithm, topological sorting.

**Symbol Tables:** static tree tables, dynamic tree tables, hash tables, hash functions, Collision resolution, overflow handling, Applications

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Fundamentals of Data Structure	Horowitz and Sahani		CBS Publications
2	Data Structures using C	Tanenbaum		Pearson Education
3	Data structure and Algorithm	Lafore		BPB Publication

**Reference Books**

S.N	Title	Authors	Edition	Publisher
1	Data Structure & Programme Design in C	Kruse, Leung, Tondo		PHI
2	Schaum's outline: Date Structures	Seymour Lipschutz		Tata Mc Graw Hill
3	An Introduction to DS with applications	Trembley and sorenson		Mc Graw Hill

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**COMPUTER ENGINEERING**

**THIRD SEMESTER**

Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE303P	Data Structures Lab	-	-	4	2	25	25	50

Course Objectives	Course Outcomes
This course is intended <ul style="list-style-type: none"><li>To emphasize the application of data structures in developing and implementing efficient programs and algorithm</li></ul>	<b>Students will be able to</b> <ul style="list-style-type: none"><li>Select appropriate data structures as applied to specified problem definition</li><li>implement Linear and Non-Linear data structures.</li><li>implement operations like searching, insertion, and deletion, traversing mechanism etc. on various data structures</li></ul>

Expt. No.	Title of the experiment
1	To design and implement basic C program using arrays & structures..
2	To implement a Menu driven program for linear & Binary search methods and demonstrate their constraints.
3	To implement a Menu driven program for Sorting methods and analyze their performances.
4	To implement a Program to demonstrate the working of a stack.
5	To implement a Program to demonstrate the working of a Queue
6	To implement a Program to apply the concepts of linked list
7	To implement the non linear data structure binary tree
8	To implement BFS and DFS in graph

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Data Structure & Programme Design in C	Kruse, Leung, Tondo		PHI
2	Schaum's outline: Date Structures	Seymour Lipschutz		Tata Mc Graw Hill
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Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE304T	Digital Circuits and Fundamentals of Microprocessor	4	-	-	4	30	70	100

Course Objectives	Course Outcomes
<p>This course is intended</p> <ul style="list-style-type: none"><li>To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronics circuits.</li><li>To impart how to design Digital Circuits.</li><li>Understand 8086 microprocessor concepts, architecture and programming.</li></ul>	<p><b>Students will be able to</b></p> <ul style="list-style-type: none"><li>represent numerical values in various number systems and will demonstrate the knowledge of: logic gates (AND, OR, NAND, NOR, XOR, XNOR), Boolean algebra, DeMorgan's Theorems, Karnaugh map.</li><li>analyze and design digital combinational circuits</li><li>analyze and design sequential digital circuits.</li><li>describe the architecture &amp; organization of 8086 microprocessor along with instruction set format.</li><li>list, describe and use different types of instructions, directives &amp; interrupts and develop assembly language program</li></ul>

**Unit I** **[7Hrs]**

**Number Systems & Code Conversion:** Number Systems & Code conversion, Boolean Algebra & Logic Gates, Truth Tables, Universal Gates, Simplification of Boolean functions, SOP and POS methods –Simplification of Boolean functions using K-maps, Signed and Unsigned Binary Numbers.

**Unit II** **[7Hrs]**

**Combinational Circuits:** Combinational Logic Circuits: Adders & Subtractors, Multiplexers, Demultiplexers, Encoders, Decoders, Programmable Logic Devices

**Unit III** **[7Hrs]**

**Sequential Circuits: Sequential Logic Circuits:** RS, Clocked RS, D, JK, Master Slave JK, T Flip-Flops, Shift Registers, Types of Shift Registers, Counters, Ripple Counter, Synchronous Counters, Asynchronous Counters, Up-Down Counter.

**Unit IV** **[7Hrs]**

**Fundamentals of 8086 Microprocessors:** 8086 microprocessor, Functional Diagram, register organization 8086, Flag register of 8086 and its functions, Addressing modes of 8086, Pin diagram of 8086, Minimum mode & Maximum mode operation of 8086, Interrupts in 8086.

**Unit V** **[8Hrs]**

**Programming of 8086 Micro-processor:** Instruction set of 8086, Assembler directives, Procedures and Macros, Simple programs involving arithmetic, logical, branch instructions, Ascending, Descending and Block move programs, String Manipulation Instructions.

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Digital Design	M. Morris Mano, Michael D. Ciletti	5 <sup>th</sup> Edition	Pearson Education
2	Digital Electronics: Principles, Devices and Applications	Anil K. Maini		John Wiley & Sons, Ltd
3	Microprocessor and Microcontrollers	N. Senthil Kumar, M. Saravanan, S Jeevanathan		Oxford Publishers
4	Advanced microprocessors & peripherals	A.K Ray and K.M. Bhurchandani	2 <sup>nd</sup> Edition	TMH

**Reference Books**

S.N	Title	Authors	Edition	Publisher
1	Digital Fundamentals –A Systems Approach	Thomas L. Floyd		Pearson
2	Fundamentals of Logic Design	Charles H. Roth	5 <sup>th</sup> Edition	Cengage Learning
3	Microprocessors and Interfacing.	D.V.Hall,	2 <sup>nd</sup> Edition	TMGH
4	The 8051 microcontroller	Kenneth.J.Ayala	3 <sup>rd</sup> Edition	Cengage Learning

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Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
CE304P	Digital Circuits and Fundamentals of Microprocessor Lab	-	-	2	1	25	25	50

Course Objectives	Course Outcomes
<p>This course is intended</p> <ul style="list-style-type: none"><li>● To introduce the basic concepts and laws involved in the Boolean algebra and logic families and digital circuits.</li><li>● To familiarize with the different logic gates, and combinational and sequential circuits utilized in the different digital circuits and systems.</li><li>● To introduce basic instruction of microprocessor.</li></ul>	<p><b>Students will be able to</b></p> <ul style="list-style-type: none"><li>● understand the Combinational Circuits using Logic Gates.</li><li>● design Arithmetic and Logical Circuit</li><li>● demonstrate understanding of flip-flops &amp; Sequential circuits .</li><li>● understand the Basic Fundamentals of 8086 Microprocessor</li></ul>

Expt. No.	Title of the experiment
1	To verify the truth table of different logic gates.
2	To study and verify the NAND & NOR gates as universal gates.
3	To study and verify truth table of Half adder and Full Adder.
4	To study and verify truth table of Multiplexer & Demultiplexer.
5	To study and verify truth table of different flip flops.
6	To study and verify 4 bit ripple counter.
7	Write and execute an ALP for addition & Subtraction of two 16 bit numbers.
8	Write and execute an ALP to find 1's complement of 16 bit a number.
9	Write and execute an ALP for sorting of data in ascending order and find Largest number in an array.
10	Mini -Project

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Digital Integrated Electronics	Herbert Taub		McGraw Hill.
2	Digital Logic and Computer Design	Morris Mano		PHI
3	Digital Integrated Electronics	Herbert Taub		McGraw Hill
4	Digital Electronics Logic and System	James Bingnell and Robert Donovan,		

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**THIRD SEMESTER**

Course Code	Course Name	Th	Tu	Pr	Credits	Evaluation		
						CA	ESE	Total
H102	Universal Human Values-II	3	-	-	3	30	70	100

Course Objectives	Course Outcomes
<ul style="list-style-type: none"><li>● Development of a holistic perspective based on self-exploration about themselves (human being), family, society and nature/existence.</li><li>● Understanding (or developing clarity) of the harmony in the human being, family, society and nature/existence</li><li>● Strengthening of self-reflection.</li><li>● Development of commitment and courage to act.</li></ul>	<p>By the end of the course, students are expected</p> <ul style="list-style-type: none"><li>● To become more aware of themselves, and their surroundings (family, society, nature);</li><li>● They would become more responsible in life, and in handling problems with sustainable solutions, while keeping human relationships and human nature in mind.</li><li>● They would have better critical ability. They would also become sensitive to their commitment towards what they have understood (human values, human relationship and human society).</li><li>● It is hoped that they would be able to apply what they have learnt to their own self in different day-to-day settings in real life, at least a beginning would be made in this direction.</li></ul>

**Unit I**

**[6Hrs]**

Purpose and motivation for the course, recapitulation from Universal Human Values-I, Self-Exploration-what is it? - Its content and process; 'Natural Acceptance' and Experiential Validation- as the process for self-exploration, Continuous Happiness and Prosperity- A look at basic Human Aspirations, Right understanding, Relationship and Physical Facility- the basic requirements for fulfilment of aspirations of every human being with their correct priority, Understanding Happiness and Prosperity correctly- A critical appraisal of the current scenario, Method to fulfil the above human aspirations: understanding and living in harmony at various levels.

**Unit II**

**[6Hrs]**

Understanding human being as a co-existence of the sentient 'I' and the material 'Body', Understanding the needs of Self ('I') and 'Body' - happiness and physical facility, Understanding the Body as an instrument of 'I' (I being the doer, seer and enjoyer), Understanding the characteristics and activities of 'I' and harmony in 'I', Understanding the harmony of I with the Body: Sanyam and Health; correct appraisal of Physical needs, meaning of Prosperity in detail, Programs to ensure Sanyam and Health.

**Unit III**

**[6Hrs]**

Understanding values in human-human relationship; meaning of Justice (nine universal values in relationships) and program for its fulfilment to ensure mutual happiness; Trust and Respect as the foundational values of relationship, Understanding the meaning of Trust; Difference between intention and competence, Understanding the meaning of Respect, Difference between respect and differentiation; the other salient values in relationship, Understanding the harmony in the society (society being an extension of family): Resolution, Prosperity, fearlessness (trust) and co-existence as comprehensive Human Goals, Visualizing a universal harmonious order in society- Undivided Society, Universal Order- from family to world family.

**Unit IV**

**[6Hrs]**

Understanding the harmony in the Nature, Interconnectedness and mutual fulfilment among the four orders of nature- recyclability and self-regulation in nature, Understanding Existence as Co-existence of mutually interacting units in all-pervasive space, Holistic perception of harmony at all levels of existence. Include practice sessions to discuss human being as cause of imbalance in nature (film "Home" can be used), pollution, depletion of resources and role of technology etc.

**Unit V**

**[6Hrs]**

Natural acceptance of human values, Definitiveness of Ethical Human Conduct, Basis for Humanistic Education, Humanistic Constitution and Humanistic Universal Order, Competence in professional ethics: a. Ability to utilize the professional competence for augmenting universal human order b. Ability to identify the scope and characteristics of people friendly and eco-friendly production systems, c. Ability to identify and develop appropriate technologies and management patterns for above production systems, Case studies of typical holistic technologies, management models and production systems, Strategy for transition from the present state to Universal Human Order: a. At the level of individual: as socially and ecologically responsible engineers, technologists and managers b. At the level of society: as mutually enriching institutions and organizations, Sum up

**Text Books**

S.N	Title	Authors	Edition	Publisher
1	Human Values and Professional Ethics	Gaur, Sangal, Bagaria	2010	Excel Books, New Delhi

**Reference Books**

S.N	Title	Authors	Edition	Publisher
1	Jeevan Vidya: Ek Parichaya	A Nagaraj	1999	Jeevan Vidya Prakashan, Amarkantak.
2	Human Values	A.N. Tripathi	2004	New Age Intl. Publishers, New Delhi
3	The Story of My Experiments with Truth	M. K Gandhi		

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