

NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics and Telecommunication Engineering	Discipline : Engineering & Technology
Level : Under Graduate	Tier : 1
Application No : 11713	Date of Submission : 02-04-2026

PART A- Profile of the Institute

A1.Name of the Institute : St. Vincent Pallotti College of Engineering and Technology	
Year of Establishment : 2004	Location of the Institute: Nagpur
A2. Institute Address :Gavsi Manapur , Wardha Road, Nagpur	
City:Nagpur	State:Maharashtra
Pin Code:441108	Website:www.stvincentngp.edu.in
Email:info@stvincentngp.edu.in	Phone No(with STD Code):-
A3. Name and Address of the Affiliating University (if any):	
Name of the University : Rashtrasant Tukdoji Maharaj Nagpur Univerisity Nag	City: Nagpur
State : Maharashtra	Pin Code: 440033
A4. Type of the Institution : Autonomous CAY(2021-22)	
A5. Ownership Status : Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **13**
- No. of PG programs: **8**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Engineering & Technology	UG	Artificial Intelligence	2021	--	Artificial Intelligence
2	Engineering & Technology	PG	Automotive Technology	2025	--	Mechanical Engineering
3	Engineering & Technology	PG	CAD/CAM	2012	2025	Mechanical Engineering
4	Engineering & Technology	UG	Civil Engineering	2018	--	Civil Engineering
5	Engineering & Technology	UG	Computer Engineering	2004	2025	Computer Engineering
6	Engineering & Technology	UG	Computer Science and Business Systems	2022	--	Computer Science and Business System
7	Engineering & Technology	UG	Computer Science and Engineering	2025	--	Computer Science and Engineering
8	Engineering & Technology	PG	Computer Science and Engineering	2012	2025	Computer Science and Engineering
9	Engineering & Technology	PG	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	2025	--	Information Technology

10	Engineering & Technology	PG	Computer Science and Engineering (Big Data Analytics)	2025	--	Computer Science and Engineering
11	Engineering & Technology	UG	Computer Science and Engineering (Cyber Security)	2022	--	Computer Science and Engineering (Cyber Security)
12	Engineering & Technology	UG	Computer Science and Engineering (Data Science)	2021	--	Computer Science and Engineering (Data Science)
13	Engineering & Technology	UG	Electrical Engineering	2004	--	Electrical Engineering
14	Engineering & Technology	UG	Electronics and Telecommunication Engineering	2004	--	Electronics and Telecommunication Engineering
15	Engineering & Technology	PG	Embedded System & VLSI	2025	--	Electronics and Telecommunication Engineering
16	Engineering & Technology	UG	Industrial IoT	2022	--	Industrial IOT
17	Engineering & Technology	UG	Information Technology	2007	--	Informatics Technology
18	Engineering & Technology	UG	Mechanical Engineering	2004	--	Mechanical Engineering
19	Engineering & Technology	PG	Power Electronics & Power Systems	2025	--	Electrical Engineering
20	Engineering & Technology	UG	Robotics and Artificial Intelligence	2025	--	Robotics and Artificial Intelligence
21	Engineering & Technology	PG	Structural Engineering	2025	--	Civil Engineering

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Electronics and Telecommunication Engineering	No	Electronics and Telecommunication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
 Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information**B1. Provide the Required Information for the Program Applied For:**

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY APPROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIME PROGRAM ACCREDITED
1	Electronics and Telecommunication Engineering	UG	2004 / --	60	Yes	2021	60	2021	06/07/MS/ENGG/2004/029 Dated 11-05-2004	Granted accreditation for 3 years for the period (specify period)	--	--	1

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY APPROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIME PROGRAM ACCREDITED
Sanctioned Intake for Last Five Years for the Electronics and Telecommunication Engineering													
Academic Year			Sanctioned Intake										
2025-26			60										
2024-25			60										
2023-24			60										
2022-23			60										
2021-22			60										
2020-21			120										

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr. Harish Rajurkar
B. Nature of appointment:	Regular
C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	60	60	60	60	60	120	120
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	59	60	58	60	59	120	124
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	6	8	7	7	12	7
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	4	3	4	3	3	6	5
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	63	69	70	70	69	138	136

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
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2025-26 (CAY)	60	59	4	105.00
2024-25 (CAYm1)	60	60	3	105.00
2023-24 (CAYm2)	60	58	4	103.33

Average [(ER1 + ER2 + ER3) / 3] = 104.44≅ 100

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	69.00	138.00	136.00
B=No. of students who graduated from the program in the stipulated course duration	62.00	111.00	115.00
Success Rate (SR)= (B/A) * 100	89.86	80.43	84.56

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 84.95

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	7.42	8.38	7.83
Y=Total no. of successful students	61.00	61.00	62.00
Z=Total no. of students appeared in the examination	63.00	62.00	63.00
API [X*(Y/Z)]	7.18	8.24	7.71

Average API[(AP1+AP2+AP3)/3] : 7.71

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2nd year/10)	7.55	7.49	6.63
Y=Total no. of successful students	68.00	67.00	64.00
Z=Total no. of students appeared in the examination	69.00	69.00	69.00
API [X * (Y/Z)]	7.44	7.27	6.15

Average API [(AP1 + AP2 + AP3)/3] : 6.95

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.31	7.12	6.45
Y=Total no. of successful students	66.00	62.00	133.00
Z=Total no. of students appeared in the examination	67.00	64.00	137.00

API [$X*(Y/Z)$]:	7.20	6.90	6.26
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Average API [(AP1 + AP2 + AP3)/3] : 6.79

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	67.00	133.00	134.00
X=No. of students placed	41.00	67.00	73.00
Y=No. of students admitted to higher studies	0.00	1.00	5.00
Z= No. of students taking up entrepreneurship	0.00	0.00	4.00
Placement Index(P) = $((X + Y + Z)/FS) * 100$:	61.19	51.13	61.19

Average Placement Index = (P_1 + P_2 + P_3)/3: 57.84 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments**(Data to be filled in for the Department and Allied Departments)****C1. Faculty details of Department and Allied Departments**

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr. Vijay Wadhai	XXXXXXXX34G	Ph.D	SGB Amravati University,Amravati	Electronics and Telecommunication Engineering	18/07/2024	1.8	Professor	Professor	18/07/2024	Regular	Yes		No
2	Dr. Harish Rajurkar	XXXXXXXX83F	Ph.D	Gondavana University, Gadchiroli	RF and Mlcrowaves,Antenna	15/06/2011	14.9	Assistant Professor	Associate Professor	26/08/2024	Regular	Yes		Yes
3	Dr.Manish Kapse	XXXXXXXX17H	Ph.D	Kalinga University	DSP , Image processing	06/08/2007	18.7	Assistant Professor	Associate Professor	01/09/2010	Regular	Yes		No
4	Dr. Kirti Vaidya	XXXXXXXX82K	Ph.D	Gondavana University, Gadchiroli	DSP	12/07/2006	19.8	Assistant Professor	Assistant Professor		Regular	Yes		No
5	Dr. Deepali Borakhade	XXXXXXXX06J	Ph.D	RTMNU,Nagpur	Wireless Communication,Antenna Design, Digital System design	15/06/2010	15.9	Assistant Professor	Assistant Professor		Regular	Yes		No
6	Dr. Hema Kale	XXXXXXXX69N	Ph.D	RTMNU,Nagpur	Wireless Communication,Embedded, Signal Processing	01/07/2016	9.8	Assistant Professor	Assistant Professor		Regular	Yes		No

7	Dr. Madhura Kulkarni	XXXXXXXX99Q	Ph.D	Vignan's Foundation for Science, Technology and Research	5G Wireless Communication	27/10/2014	11.4	Assistant Professor	Assistant Professor		Regular	Yes		No
8	Dr.Avinash Lambat	XXXXXXXX70P	Ph.D	SGB Amravati University,Amravati	Image Processing	01/07/2006	19.8	Assistant Professor	Assistant Professor		Regular	Yes		No
9	Dr. Shiju Samuel	XXXXXXXX96M	Ph.D	Vignan's Foundation for Science, Technology and Research	Image Processing	01/07/2016	9.9	Assistant Professor	Assistant Professor		Regular	Yes		No
10	Ms. Mrunalini Buradkar	XXXXXXXX23G	M.Tech	RTMNU, Nagpur	VLSI	01/07/2009	16.8	Assistant Professor	Assistant Professor		Regular	Yes		No
11	Ms. Dakshata Gharode	XXXXXXXX70J	M.Tech	RTMNU, Nagpur	Wireless Communication	21/12/2011	14.3	Assistant Professor	Assistant Professor		Regular	Yes		No
12	Mr. Ravindra Deshmukh	XXXXXXXX78G	M.E.	RTMNU, Nagpur	Core Electronics	15/07/2004	21.8	Assistant Professor	Assistant Professor		Regular	Yes		No
13	Dr..Ashutosh Werulkar	XXXXXXXX84G	Ph.D	VNIT, Nagpur	Solar PV system, Embedded System, Smart EV	16/08/2006	19.7	Assistant Professor	Professor	02/08/2021	Regular	Yes		No
14	Ms. Nishat Faizee	XXXXXXXX82Q	M.Tech	Savitribai Phule Pune University	Signal Processing, Communication	21/12/2011	14.3	Assistant Professor	Assistant Professor		Regular	Yes		No
15	Dr. Swati Mirlekar	XXXXXXXX50A	Ph.D	RKDF (SRK) , BHOPAL	Electronics and Communication Engineering	23/06/2014	11.9	Assistant Professor	Assistant Professor		Regular	Yes		No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)**C**= No. of Students in UG 3rd year (ST)**D**= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year**B**= No. of Students in PG 2nd yearStudent Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	66	66	66
UG1.C	66	66	66
UG1.D	66	66	132
UG1: Electronics and Telecommunication Engineering	198	198	264
PG1.A	12	0	0
PG1.B	0	0	0
PG1: Embedded System & VLSI	12	0	0
DS=Total no. of students in all UG and PG programs in the Department	210	198	264
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 210	S2= 198	S3= 264
DF=Total no. of faculty members in the Department	15	15	14
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 15	F2= 15	F3= 14
FF=The faculty members in F who have a 100% teaching load in the first-year courses	0	0	0
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 14.00	SFR2= 13.20	SFR3= 18.86
Average SFR for 3 years	SFR= 15.35		

C3. Faculty Qualification

- Faculty qualification index (FQI) = $2.5 * [(10X + 4Y)/RF]$ where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 \times [(10X + 4Y) / RF]$
2025-26(CAY)	10	5	10.00	30.00
2024-25(CAYm1)	9	6	9.00	31.67
2023-24(CAYm2)	5	9	13.00	16.54

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = $1/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:}$.
- RF2= No. of Associate Professors required = $2/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:}$.
- RF3= No. of Assistant Professors required = $6/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:}$.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	1.00	2.00	2.00	2.00	7.00	11.00
2024-25	1.00	2.00	2.00	2.00	6.00	11.00
2023-24	1.00	1.00	2.00	1.00	8.00	12.00
Average	RF1=1.00	AF1=1.67	RF2=2.00	AF2=1.67	RF2=7.00	AF2=11.33

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr. K.V. RamSubramaniam	Corporate trainer	Private Nagpur	VEC	25.00
2	Mr. K.V. RamSubramaniam	Corporate trainer	Private Nagpur	VEC_II	25.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr. A. Taywade	Corporate trainer	Private Nagpur	Universal Human values	32.00
2	Mr. Anil Hood	Corporate trainer	Private Nagpur	IKS	20.00
3	Mr. Sandeep Darwhekar	Corporate trainer	Beta Computronix PVT Ltd, Nagpur	TSD	14.00
4	Sarang Gurve	Industry Trainer	IT Networkz	Web Technologies	50.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Sarang Gurve	Industry Trainer	IT Networkz	DBMS	30.00
2	Mr. K.V. RamSubramaniam	Corporate trainer	Private Nagpur	ETC	25.00
3	Mr. A. Taywade	Corporate trainer	Private Nagpur	UHV-II	20.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	5	9	9
2	No. of peer reviewed conference papers published	7	7	7
3	No. of books/book chapters published	6	3	3

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. A. Werulkar	0	ET	Low cost converted solar power Electric tricycle	Maharogi Sewa Samiti Anandwan, Warora	1 year	25000.00
						Amount received (Rs.):25000.00

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
0	0	0	0	0	0	0.00
						Amount received (Rs.):0.00

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. A. Werulkar	NA	ET	Design and development of electric bicycle for Panzhari lodhi village	IIT, Delhi Unnat bhara Abhiyaan	1 year	50000.00
						Amount received (Rs.):50000.00

Total Amount (Lacs) Received for the Past 3 Years: 75000.00**Note*:**

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
						Amount received (Rs.):0

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
						Amount received (Rs.):0

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
						Amount received (Rs.):0

Total amount (Lacs) received for the past 3 years: 0

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr A S Werulkar	Society based projects under Unnat Bharat Abhiyan	1 year	1.00	1.00	Experiential learning on different society based projects
			Amount received (Rs.): 1.00		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr A S Werulkar	Society based projects under Unnat Bharat Abhiyan	1 year	0.92	0.92	Experiential learning on different society based projects
			Amount received (Rs.): 0.92		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
0	0	0	0.00	0.00	0
			Amount received (Rs.): 0.00		

Total amount (Lacs) received for the past 3 years : 1.92

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification

1	Digital Circuits Lab	4	• Digital IC trainer Kit Model F41A, • Counter Kits	12 HRS(ODD)	Mr. Abhijit B. Lonare	Lab Assistant	BE (Electronics)
2	Electronic Devices Lab	4	• Sciencetech 403 DSO 100 MHz • Banix made Electronic devices kits	14 HRS(ODD)	Mr. Omprakash Bhagwæ	Lab Assistant	Diploma (Electronics an
3	Analog Circuit and design Lab	4	• Op-AMP Trainer and application kits	06 HRS(EVEN)	Mr. Omprakash Bhagwæ	Lab Assistant	Diploma (Electronics ar
4	Microprocessor & Interfacing Lab	4	• Flowcode IDE • ARM Cortex-M4 STM32F407 Development Boards • Anshuman	06 HRS(ODD)	Mr. Shailendra Suple	Lab Assistant	Diploma (Electronics an
5	Analog and Digital Communication Lab	4	• Analog and Digital Modulation transmitter and Receiver	12 HRS(ODD)	Mr. Abhijit B. Lonare	Lab Assistant	BE (Electronics)
6	Digital Signal Processing Lab	4	• MATLAB Software toolboxes • DSP starter kits 5416 and 6713 by Texas instrument	06 HRS(EVEN)	Mr. Vishal Chichalkar	Lab Assistant	Diploma (Electronics an
7	Digital System Design Lab	4	• Xilinx Spartan 3E Basys 2FPGA board • Artix-7 FPGA Development Platform • Vivado	06 HRS(EVEN)	Mr. Vishal Chichalkar	Lab Assistant	Diploma (Electronics an
8	Microwave and Radar Engineering Lab	4	• Sciencetech ,NVIS Microwave test Bench - GUNN and Klystron • Antenna trainer • NVIS	06 HRS(ODD)	Mr. Omprakash Bhagwæ	Lab Assistant	Diploma (Electronics an
9	Computing LAB	1	Multisim 60 desktop PCs	12 HRS(ODD)	Mr. Vishal Chichalkar	Lab Assistant	Diploma (Electronics an
10	Project Lab	4	Multisim 60 desktop PCs • Lorawan Gateway • Lorawan Server • Lora duno kit • Xilinx CPLD kit •	14 HRS(ODD)	Mr. Shailendra Suple	Lab Assistant	Diploma (Electronics an

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Digital Circuits and Fundamental of Microprocessor Lab (F-01B)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover, or attempt to repair any equipment. • Each group is responsible for the Lab bench. When the lab exercise is over, all issued instruments must be turned off and returned. • Do not move instruments from one lab station to another lab station. • While handling any electronics accessories do not increase the voltage to their specified limits. For knowing the limits of voltages of these accessories, the study the data sheets in detail before starting hardware experimentation. • If any component temperature is increasing because of the wrong connection or short circuit, remove the connection and verify the circuit for any wrong connection or short circuit. • Anyone violating any rules or regulations will not be allowed access to the lab facilities. Human Safety • Do not tamper with or remove security straps, locks, or other security devices • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.
2	Communication Electronics Lab (F-01B)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover or attempt to repair any equipment. • If you suspect something is not operating correctly, report it to the lab attendant or Faculty • When the lab exercise is over, all instruments must be turned off. • Be especially careful when using connectors to avoid breaking pins and cross-threading. • Do not move instruments from one lab station to another lab station. • Do not tamper with or remove security straps, locks, or other security devices. Human Safety Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.

3	Microwave and Radar Engineering Lab(F-01 C)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover or attempt to repair any equipment. • If you suspect something is not operating correctly, report it to the lab assistant or Faculty. • When the lab exercise is over, all instruments must be turned off. • Be especially careful when using connectors to avoid breaking pins and cross-threading. • Do not move instruments from one lab station to another lab station. • Do not tamper with or remove security straps, locks, or other security devices. Human Safety • Do not look into the waveguide when the source is ON. It may harm the eyes
4	Electronic and Devices Lab (F-01 C)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover or attempt to repair any equipment. • If you suspect something is not operating correctly, report it to the lab attendant or Faculty • When the lab exercise is over, all instruments must be turned off. • Be especially careful when using connectors to avoid breaking pins and cross-threading. • Do not move instruments from one lab station to another lab station. • Do not tamper with or remove security straps, locks, or other security devices. Human Safety • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.
5	Analog Circuit and Design Lab (F-01 C)	Instrument Safety • Do not open/remove the cover, or attempt to repair any Trainer kit, CRO, Function Generator, Power supply. • Do not move Trainer kit, CRO, Function Generator, Power supply from one lab station to another lab station. • Do not give AC supply to DC circuit. • Give proper value of DC supply to Electronic circuit after checking all connections. • Set the knob of multimeter to proper position for AC & DC measurements before its use to avoid the damage of multimeter. • Do not play with the knob of Trainer kit, CRO, Function Generator, Power supply, multimeter. • If any semiconductors, Integrated circuit(IC) becomes hot or fume's comes out report immediately to the Lab Assistant. Human Safety • Do not switch on or plug in any AC power supply without help of Lab Assistant. • Always perform experiments with only dry hand with wearing shoes
6	Digital Signal Processing and processer Lab(Computer Center F-03 A)	Instrument Safety • Do not change any setting of the any computer in the lab. • No one is allowed to delete information from the computer unless otherwise if it is given as an instruction. • Anyone violating any rules or regulations will not be allowed access to the lab facilities. • Shut down the computer after use by closing all windows. • Handle Texas instruments kits with care. Human Safety • When unplugging a power cord, pull on the plug, not on the cable.
7	Advanced Digital System Design (Computer Center F03 A)	Instrument Safety • Do not change any setting of the any computer in the lab. • No one is allowed to delete information from the computer unless otherwise if it is given as an instruction. • Anyone violating any rules or regulations will not be allowed access to the lab facilities. • Shut down the computer after use. • Always maintain an extra copy of all your important data files. Human Safety • When unplugging a power cord, pull on the plug, not on the cable. • Do not insert metal objects such as clips, pins and needles into the computer casings. They may cause fire. • Do not tamper with or remove security straps, locks or other security devices. • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.
8	Project Lab (F 08)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover, or attempt to repair any equipment. • Each group is responsible for the Lab bench. When the lab exercise is over, all issued instruments must be turned off and returned. • Do not move instruments from one lab station to another lab station. • While handling any electronics accessories do not increase the voltage to their specified limits. For knowing the limits of voltages of these accessories, the study the data sheets in detail before starting hardware experimentation. • If any component temperature is increasing because of the wrong connection or short circuit, remove the connection and verify the circuit for any wrong connection or short circuit. • Anyone violating any rules or regulations will not be allowed access to the lab facilities. Human Safety • Do not tamper with or remove security straps, locks, or other security devices • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.
9	Microprocessor & Microcontroller Lab (F 08)	Instrument Safety • Report any broken equipment or defective parts to the lab instructor. Do not open/remove the cover, or attempt to repair any equipment. • When the lab exercise is over, all instruments must be turned off. • Do not move instruments from one lab station to another lab station. • While handling any electronics accessories do not increase the voltage to their specified limits. For knowing the limits of voltages of these accessories, the study the data sheets in detail before starting hardware experimentation. • If any component temperature is increasing because of the wrong connection or short circuit, remove the connection and verify the circuit for any wrong connection or short circuit. • Anyone violating any rules or regulations will not be allowed access to the lab facilities Human Safety • Do not tamper with or remove security straps, locks or other security devices. • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.

10	Computing Lab	Instrument Safety • Do not change any setting of the any computer in the lab. • No one is allowed to delete information from the computer unless otherwise if it is given as an instruction. • Anyone violating any rules or regulations will not be allowed access to the lab facilities. • Shut down the computer after use by closing all windows. Human Safety • When unplugging a power cord, pull on the plug, not on the cable. • Do not insert metal objects such as clips, pins and needles into the computer casings. They may cause fire. • Do not tamper with or remove security straps, locks or other security devices. • Students should be careful while handling the equipment's with AC supply. Do not touch any bare wire with bare hand while experimentation.
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D3. Project Laboratory/Research Laboratory

The Project Laboratory enables students to apply the theoretical concepts learned throughout the Electronics and Telecommunication Engineering program to solve real-world engineering problems. Students work individually or in teams to design, develop, test, and implement innovative projects in domains such as Embedded Systems, IoT, Communication Systems, VLSI, Signal Processing, and Wireless Technologies. The laboratory enhances technical competency, problem-solving ability, teamwork, project management skills, and the effective use of modern engineering tools while encouraging creativity, research orientation, and industry readiness.

Objectives of Project Laboratory

1. To provide students with hands-on experience in designing and implementing Electronics and Telecommunication Engineering projects.
2. To develop problem-solving, teamwork, project management, and communication skills through real-time project execution.
3. To encourage innovation, research aptitude, and the effective use of modern engineering tools and technologies.

Course Outcomes

After completion of the Project Laboratory, students will be able to:

1. Design and develop a solution for real-world engineering problems using appropriate hardware and software tools.
2. Apply technical knowledge, modern tools, and project management principles for successful project implementation and testing.
3. Demonstrate teamwork, effective communication, documentation, and presentation skills during project execution.

Total area of project laboratory is 87 square meter.



Project Laboratory

Following Major equipments and instruments are there in a lab.

Sr. No.	Major Equipments
1	Lorawan Gateway
2	Lorawan Server
3	Lora Duino Development Board
4	ESP 32 Lora Development
5	Node MCU Development
6	Raspberry Pi Development

7	Textronics Make Oscilloscope 100 MHz
8	Function Generator-25 MHZ
9	LCR meter
10	Dual Power Supply
11	CEESTA 2 AMP Dimmer
12	Dual Channel Oscilloscope
13	Digital Panel meter
14	Current transducer, Clamp meter, Digital Ammeter
15	Universal Programmer, EPROM eraser
16	Xilinx CPLD kit Xilinx FPGA kit

The department also has a dedicated Microchip Research Laboratory where students actively work on research-oriented projects, innovative product development, and participation in hackathons and technical competitions. The laboratory provides a collaborative environment for students to carry out project-related research activities using modern hardware and software tools. Equipped with advanced resources and development platforms, the lab supports innovation, experimentation, prototyping, and skill enhancement in emerging areas of Electronics and Telecommunication Engineering.

Utilization of Project Laboratory

The Project Laboratory is effectively utilized for carrying out mini projects, major projects, research activities, prototype development, and innovative product design in various domains of Electronics and Telecommunication Engineering. Students use the laboratory facilities for design, simulation, implementation, testing, and validation of hardware and software-based systems using modern engineering tools and technologies.

Department of Electronics and Telecommunication Engineering
LAB-TIME TABLE
WEP: 15th July 2024
Academic year: 2024-25
Lab: MPMC & Project Lab

Day	8:00 AM to 10:00 AM	10:00 AM to 11:00 AM	11:00 AM to 12:00 Noon	12:00 Noon to 1:00 PM	1 PM - 2 PM	2:00 PM to 3:00 PM	3:00 PM to 4:00 PM
MON			MPMC [E1]- AL			Project-0 [E2]- DR	
TUE			Project-0 [E1]- DR			Micro Project-4 [E4]- HK	
WED			Micro Project-1 [E2]- AW			Project-4 [E3]- DR	
THU		Micro Project-1 [E3]- AW				MPMC [E2]- HK	
FRI			Micro Project-4 [E1]- AW			MPMC [E1]- HK	

ABBREVIATIONS
MPMC Microprocessor and Microcontroller

ABBREVIATIONS
DR Prof. Dr. H. Rajurkar
AW Prof. Dr. A. Wankar
AL Prof. A. Lantke
DB Prof. Dr. D. Borakhade
HK Prof. Dr. H. Kulkarni

Prof. Nishat Faizee
Time Table Incharge

Dr. Deepesh Borakhade
Academic Coordinator

Dr. Harish Rajurkar
HOD/ETEC
Electronics and Telecommunication Engineering
St. Vincent's College of Engineering & Technology, Nagpur

Sample Project Lab Utilization- Time Table

Apart from the regular Project Laboratory timetable, the laboratory is extensively utilized by students beyond academic hours for carrying out their mini and major projects, research activities, and prototype development. Students actively use the laboratory facilities to work on innovative ideas and practical implementations in various domains of Electronics and Telecommunication Engineering. The laboratory also supports student participation in various technical events and competitions such as project competitions, hackathons, E-Yantra competitions, innovation challenges, and other technical activities. The availability of modern hardware and software tools provides an effective environment for experimentation, collaborative learning, skill enhancement, and research-oriented project development.

Relevance to PO/PSOs

CO-PO/PSO Mapping and Extent of Correlation for Project Laboratory

The Project Laboratory plays a vital role in achieving the Program Outcomes (POs) and Program Specific Outcomes (PSOs) by providing students with opportunities to apply engineering knowledge and practical skills to solve real-world problems. Through project planning, design, implementation, testing, analysis, documentation, and presentation, students enhance their technical competence, teamwork, communication abilities, and project management skills.

The Project Lab demonstrates a strong correlation with outcomes related to engineering knowledge (PO1), modern tool usage (PO5), individual and teamwork (PO8/9), communication skills (PO9/10), project management (PO10/11), and lifelong learning (PO11/12). The laboratory also contributes to the attainment of PSOs by enabling students to apply domain-specific knowledge, software tools, hardware implementation techniques, and emerging technologies in project development.

The mapping indicates that the Project Laboratory supports practical learning, innovation, teamwork, communication, and project execution capabilities essential for professional engineering practice.

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members $((NS1*0.8) + (NS2*0.2)) / (\text{No. of required faculty (RF4)})$; Percentage= $((NS1*0.8) + (NS2*0.2)) / RF$
2023-24(CAYm2)	720	36	19	35	62
2024-25(CAYm1)	720	36	19	48	69
2025-26(CAY)	780	39	20	47	65

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	150000000	118234860	155000000	53529901	85000000	23206738	20000000	20086691
Library	1000000	114174	1600000	823097	1700000	817711	1300000	1334697
Laboratory equipment	39000000	17679448	28000000	17333423	28000000	38463020	25700000	28042864
Teaching and non-teaching staff salary	296247000	198374839	233200000	256244944	202900000	221033239	163500000	180533676
Outreach Programs	0	0	0	0	0	0	0	0

R&D	11250000	25088063	3000000	3558587	3000000	4349831	500000	2135971
Training, Placement and Industry linkage	6250000	7022914	16700000	8008155	8380000	5263237	4600000	8953933
SDGs	0	0	0	0	0	0	0	0
Entrepreneurship	0	0	0	0	0	0	0	0
Others, specify	71500000	73656601	75027000	67384713	66576000	60026946	46920000	74832552
Total	575247000	440170899	512527000	406882820	395556000	353160722	262520000	315920384

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	700000	507105	610000	931558	600000	472067	900000	2113087
Software	500000	867300	500000	0	20000	0	300000	0
SDGs	0	0	0	0	0	0	0	0
Support for faculty development	365000	164333	635000	200049	356000	122739	407500	163623
R & D	200000	24286	440000	17440	350000	14000	130000	178294
Industrial Training, Industry expert, Internship	565000	249870	935000	726696	1756000	643395	507500	586268
Miscellaneous Expenses*	780000	915420	579000	605865	1825000	2555192	268000	1654800
Total	3110000	2728314	3699000	2481608	4907000	3807393	2513000	4696072